

SPECIFICATIONS

Customer	
Product Name	Thin Film RF Inductor
Sunlord Part Number	SDCL0402Q-P01 Series
Customer Part Number	

New Released, Revised]

SPEC No.: SDCL0220220000

【This SPEC is total 12 pages including specifications and appendix. 】

【RoHS Compliant Parts 】

Approved By	Checked By	Issued By

Shenzhen Sunlord Electronics Co., Ltd.

Address: Sunlord Industrial Park, Dafuyuan Industrial Zone, Baoan, Shenzhen, China 518110
 Tel: 0086-755-29832333 Fax: 0086-755-82269029 E-Mail: sunlord@sunlordinc.com

【For Customer approval Only】 Date: _____

Qualification Status: Full Restricted Rejected

Approved By	Verified By	Re-checked By	Checked By

Comments:

【Version change history】

Rev.	Effective Date	Changed Contents	Change reasons	Approved By
01	/	New release	/	Xiangdong Zeng

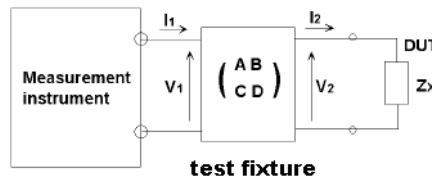
Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially Super reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

1. Aircraft equipment
2. Aerospace equipment
3. Undersea equipment
4. nuclear control equipment
5. military equipment
6. Power plant equipment
7. Medical equipment
8. Transportation equipment (automobiles, trains, ships, etc.)
9. Traffic signal equipment
10. Disaster prevention / crime prevention equipment
11. Data-processing equipment
12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

Measuring Method of Inductance

- a. Residual elements and stray elements of test fixture can be described by F-parameter as shown in the following:



$$\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} V_2 \\ I_2 \end{bmatrix}$$

$$\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} AV_2 + BI_2 \\ CV_2 + DI_2 \end{bmatrix}$$

Measured open impedance: $Z_{om} = \frac{A}{C}$

Measured short impedance: $Z_{sm} = \frac{B}{D} \approx -Z_{sc}$ (when uses short chip to short)

Measured short ship impedance: Z_{sc}

Measured value: $Z_{xm} = V_1 / I_1$

Impedance of DUT: $Z_x = V_2 / I_2$

- b. The relation between Z_x and Z_{om} , Z_{sm} , Z_{xm} is shown in the following:

$$Z_x = \frac{V_2}{I_2} = \frac{D}{A} * \frac{\frac{V_1}{I_1} - \frac{B}{D}}{1 - \frac{V_1}{I_1} * \frac{C}{A}} = \frac{D}{A} * \frac{Z_{xm} - \frac{B}{D}}{1 - Z_{xm} * \frac{C}{A}} = \frac{D}{A} * \frac{Z_{xm} - Z_{sm}}{1 - Z_{xm} / Z_{om}}$$

- c. L_x should be calculated with the following equation:

$$L_x = \frac{\ln(Z_x)}{2\pi f} = \frac{\ln(Z_{xm} + Z_{sc})}{2\pi f} = \frac{\ln(Z_{xm})}{2\pi f} + \frac{\ln(Z_{sc})}{2\pi f} = L_{xm} + L_{sc}$$

L_{xm} : Measured chip inductor inductance

L_{sc} : Measured short chip inductance

L_x : Nominal Inductance of chip inductor

Compensation Value (Lsc) of Short Chip

Series	Compensation Value
SDCL0402Q-P01	0.19nH

1. Scope

This specification applies to SDCL0402Q-P01 series of thin film radio frequency inductor.

2. Product Description and Identification (Part Number)

- 1) Description
SDCL0402Q-P01 series of thin film radio frequency inductor.

- 2) Product Identification (Part Number)

SDCL
0402
Q
XXX
□
◎
01
 ① ② ③ ④ ⑤ ⑥ ⑦

①	Type
SDCL	Super Q Ceramic Chip Inductor

②	External Dimensions (L X W) (mm)	
0402 [01005]	0.4 X 0.2	

③	Applications and Characteristics Code	
Q	Chip Thickness=0.23mm	

④	Nominal Inductance	
Example	Example	
3N9	3N9	
10N	10N	

⑤	Inductance Tolerance	
B、C、S	±0.1、±0.2、±0.3nH	
G、H、J	±2%、±3%、±5%	

⑥	Packing	
P	Plastic Tape Carrier Package	

⑦	Serial Code	
01	Internal code	

3. Electrical Characteristics

Please refer to **Appendix A** (Page10-12).

- 1) Operating and storage temperature range (individual chip without packing): -55°C~ +125°C,
- 2) Storage temperature range (packaging conditions): -10°C~+40°C and RH 70% (Max.)

4. Shape and Dimensions

- 1) Dimensions and recommended PCB pattern for reflow soldering: See **Fig.4-1**, **Fig.4-2** and **Table 4-1**.
- 2) Structure: See **Fig. 4-3** and **Fig. 4-4**.

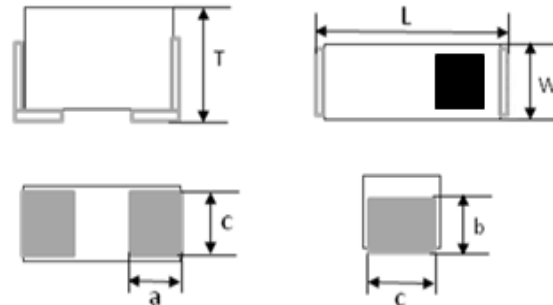
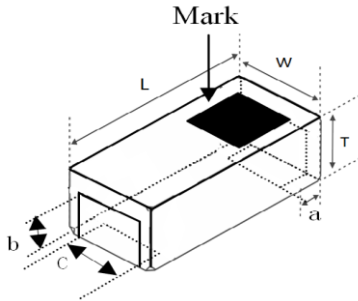


Fig. 4-1

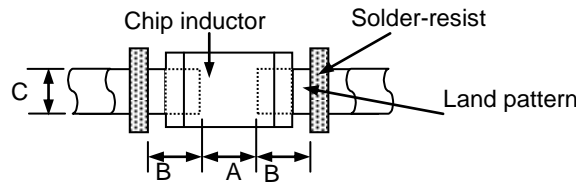
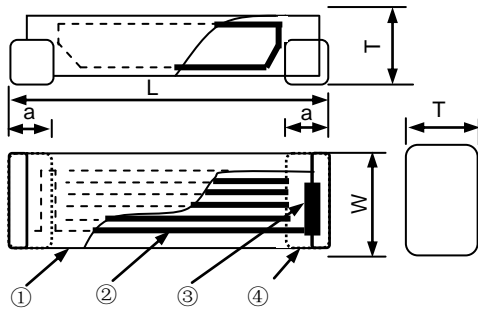


Fig. 4-2

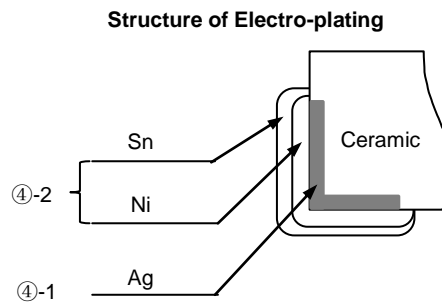
[Table 4-1]

Unit: mm [inch]

Type	L	W	T	a	b	c	A	B	C
0402 [01005]	0.4±0.02 [.016±.0008]	0.2±0.02 [.008±.0008]	0.23±0.02 [.009±.0008]	0.11±0.03 [.005±.0010]	0.11±0.03 [.005±.0010]	0.17±0.03 [.006±.0010]	0.15~0.19	0.18~0.22	0.18~0.22



[Fig 4-3]



[Fig 4-4]

- ① Ceramic for SDCL Series
- ② Internal electrode (Ag)
- ③ Pull out electrode (Ag)
- ④-1 Terminal electrode: Inside (Ag)
- ④-2 Outside (Electro-plating Ni-Sn)

- 3) Material Information: See **Table 4-2**

Table 4-2

Code	Part Name	Material Name
①	Ceramic Body	Ceramic Powder
②	Inner Coils	Silver Paste
③	Pull-out Electrode (Ag)	Silver Paste
④-1	Terminal Electrode: Inside Ag	Silver Paste
④-2	Electro-Plating: Ni/Sn plating	Plating Chemicals

- 4) Soldering Notice: The surface with the mark should be on the two beside when soldering

5. Test and Measurement Procedures

5.1 Test Conditions

5.1.1 Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

- a. Ambient Temperature: $20 \pm 15^\circ\text{C}$
- b. Relative Humidity: $65 \pm 20\%$
- c. Air Pressure: 86 KPa to 106 KPa

5.1.2 If any doubt on the results, measurements/tests should be made within the following limits:

- a. Ambient Temperature: $20 \pm 2^\circ\text{C}$
- b. Relative Humidity: $65 \pm 5\%$
- c. Air Pressure: 86KPa to 106 KPa

5.2 Visual Examination

- a. Inspection Equipment: 60 X magnifier

5.3 Electrical Test

5.3.1 DC Resistance (DCR)

- a. Refer to **Appendix A**.
- b. Test equipment (Analyzer): Super Accuracy Milliohmmeter-HP4338B or equivalent.

5.3.2 Inductance (L)

- a. Refer to **Appendix A**.
- b. Test equipment: Super Accuracy RF Impedance /Material Analyzer-E4991A+16198A or equivalent.
- c. Test signal: -20dBm or 50mV
- d. Test frequency refers to Appendix A.
- e. Short bar residual inductance=0.19nH

5.3.3 Q Factor (Q)

- a. Refer to **Appendix A**.
- b. Test equipment: Super Accuracy RF Impedance /Material Analyzer-E4991A+16198A or equivalent.
- c. Test signal: -20dBm or 50mV
- d. Test frequency refers to Appendix A.

5.3.4 Self-Resonant Frequency (SRF)

- a. Refer to **Appendix A**.
- b. Test equipment: Agilent 8719ES or equivalent.
- c. Test signal: -20 dBm or 50 mV

5.3.5 Rated Current

- a. Refer to **Appendix A**.
- b. Test equipment (see **Fig. 5.3.5-1**): Electric Power, Electric current meter, Thermometer.
- c. Measurement method (see **Fig. 5.3.5-1**):
 1. Set test current to be 0 mA.
 2. Measure initial temperature of chip surface.
 3. Gradually increase voltage and measure chip temperature for corresponding current.
- d. Definition of Rated Current(Ir): Ir is direct electric current as chip surface temperature rose just 20°C against chip initial surface temperature(T_a) (see **Fig. 5.3.5-2**).

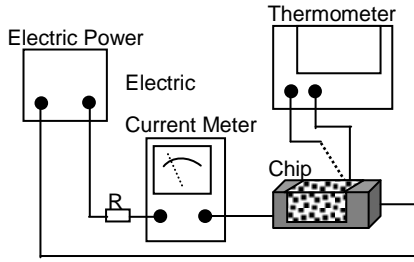


Fig. 5.3.5-1

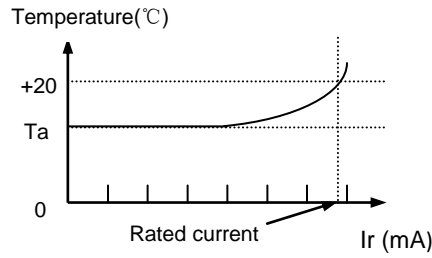
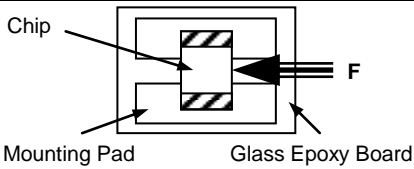
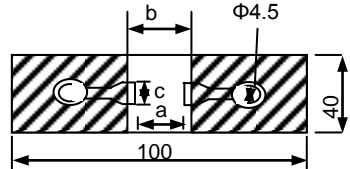
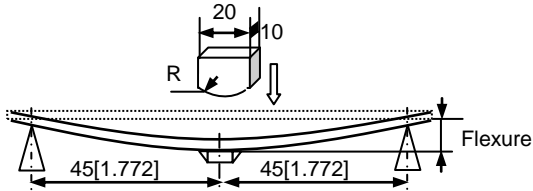
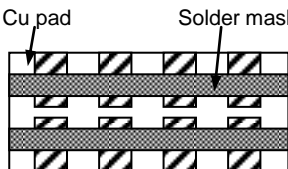
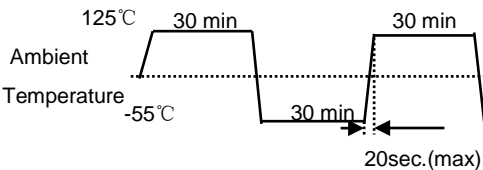


Fig. 5.3.5-2

5.4 Reliability Test

Items	Requirements	Test Methods and Remarks												
5.4.1 Terminal Strength	<p>No removal or split of the termination or other defects shall occur.</p>  <p>Fig.5.4.1-1</p>	<ol style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig. 5.4.1-1) using leadfree solder. Then apply a force in the direction of the arrow. ② 1N force for SDCL0402Q-P01 series. ③ Keep time: 10±1s ④ Speed: 1.0mm/s. 												
5.4.2 Resistance to Flexure	<p>No visible mechanical damage.</p> <table border="1" data-bbox="319 851 750 974"> <thead> <tr> <th colspan="4">Unit: mm [inch]</th> </tr> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td>0402[01005]</td> <td>0.18</td> <td>0.8</td> <td>0.2</td> </tr> </tbody> </table>  <p>Fig. 5.4.2-1</p>	Unit: mm [inch]				Type	a	b	c	0402[01005]	0.18	0.8	0.2	<ol style="list-style-type: none"> ① Solder the inductor to the test jig (glass epoxy board shown in Fig. 5.4.2-1) Using a leadfree solder. Then apply a force in the direction shown Fig. 5.4.2-2. ② Flexure: 2mm. ③ Pressurizing Speed: 0.5mm/sec. ④ Keep time: 30 sec.  <p>Fig. 5.4.2-2</p>
Unit: mm [inch]														
Type	a	b	c											
0402[01005]	0.18	0.8	0.2											
5.4.3 Vibration	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Inductance change: Within ±10%. ③ Q factor change: Within ±20%.  <p>Fig. 5.4.3-1</p>	<ol style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig. 5.4.3-1) using leadfree solder. ② The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. ③ The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours). 												
5.4.4 Dropping	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Inductance change: Within ±10%. ③ Q factor change: Within ±20%. 	<p>Drop chip inductor 10 times on a concrete floor from a height of 100 cm.</p>												
5.4.5 Temperature	<p>Inductance change should be within ±10% of initial value measuring at 20°C.</p>	<p>Temperature range: SDCL0402Q-P01: -55°C to +125°C, Reference temperature: +20°C</p>												
5.4.6 Solderability	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Wetting shall exceed 95% coverage. 	<ol style="list-style-type: none"> ① Solder temperature: 240±2°C ② Duration: 3 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight. 												
5.4.7 Resistance to Soldering Heat	<ol style="list-style-type: none"> ① No visible mechanical damage. ② Wetting shall exceed 75% coverage. ③ Inductance change: Within ±10%. ④ Q factor change: Within ±20%. 	<ol style="list-style-type: none"> ① Solder temperature: 260±3°C ② Duration: 5 sec. ③ Solder: Sn/3.0Ag/0.5Cu. ④ Flux: 25% Resin and 75% ethanol in weight. ⑤ The chip shall be stabilized at normal condition for 1~2 hours before measuring. 												

<p>5.4.8 Thermal Shock</p>	<p>① No mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>  <p style="text-align: center;">Fig. 5.4.8-1</p>	<p>① Temperature, Time: (See Fig. 5.4.8-1) SDCL0402Q-01: -55°C for 30 ± 3 min \rightarrow 125°C for 30 ± 3 min, ② Transforming interval: Max. 20 sec. ③ Tested cycle: 100 cycles. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
<p>5.4.9 Resistance to Low Temperature</p>	<p>① No mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>	<p>① Temperature: $-55\pm 2^{\circ}\text{C}$, ② Duration: 1000^{+24} hours. ③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
<p>5.4.10 Resistance to Super Temperature</p>	<p>① No mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>	<p>① Temperature: $125\pm 2^{\circ}\text{C}$, ② Duration: 1000^{+24} hours. ③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
<p>5.4.11 Damp Heat (Steady States)</p>	<p>① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>	<p>① Temperature: $60\pm 2^{\circ}\text{C}$ ② Humidity: 90% to 95% RH. ③ Duration: 1000^{+24} hours. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
<p>5.4.12 Loading Under Damp Heat</p>	<p>① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>	<p>① Temperature: $60\pm 2^{\circ}\text{C}$ ② Humidity: 90% to 95% RH. ③ Duration: 1000^{+24} hours. ④ Applied current: Rated current. ⑤ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
<p>5.4.13 Loading at Super Temperature (Life Test)</p>	<p>① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$. ③ Q factor change: Within $\pm 20\%$.</p>	<p>① Temperature: $125\pm 2^{\circ}\text{C}$, ② Duration: 1000^{+24} hours. ③ Applied current: Rated current. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>

6. Packaging and Storage

6.1 Packaging

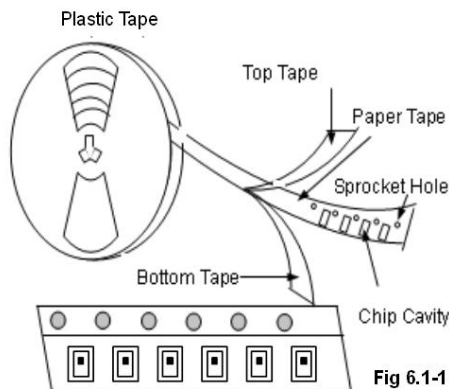
Tape Carrier Packaging:

Packaging code: P

- a. Tape carrier packaging are specified in attached figure Fig.6.1-1~3
- b. Tape carrier packaging quantity please see the following table:

Type	0402[01005]
Thickness (mm)	0.2 \pm 0.05
Tape	Plastic Tape
Quantity	30K

(1) Taping Drawings (Unit: mm)



Remark: The sprocket holes are to the right as the tape is pulled toward the user.

(2) Taping Dimensions (Unit: mm)

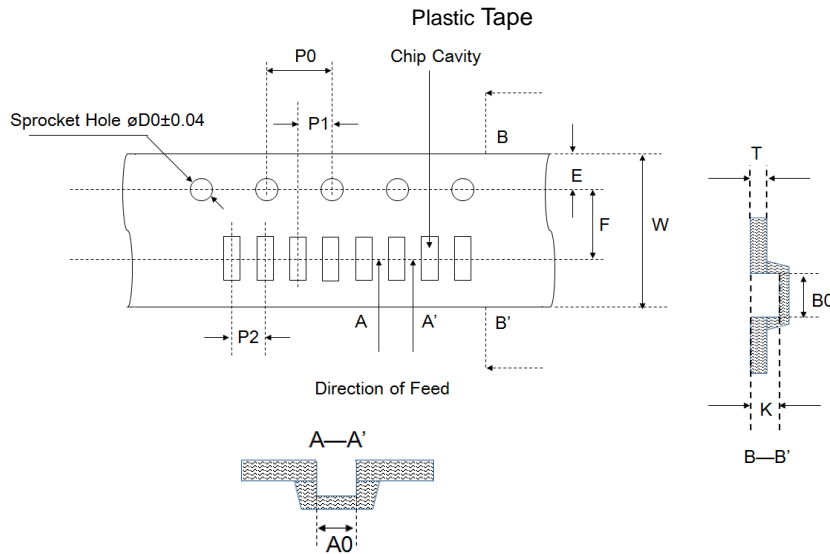


Fig. 6.1-2

Table 6.1-1

Unit:mm

Type	A0	B0	T	W	K	P0	P1	P2	D0	F	E
0402	0.24±0.02	0.44±0.02	0.2±0.05	4.0±0.05	0.27±0.02	2.0±0.04	1.0±0.03	1.0±0.03	0.80±0.04	1.8±0.03	0.9±0.05

(2) Reel Dimensions (Unit: mm)

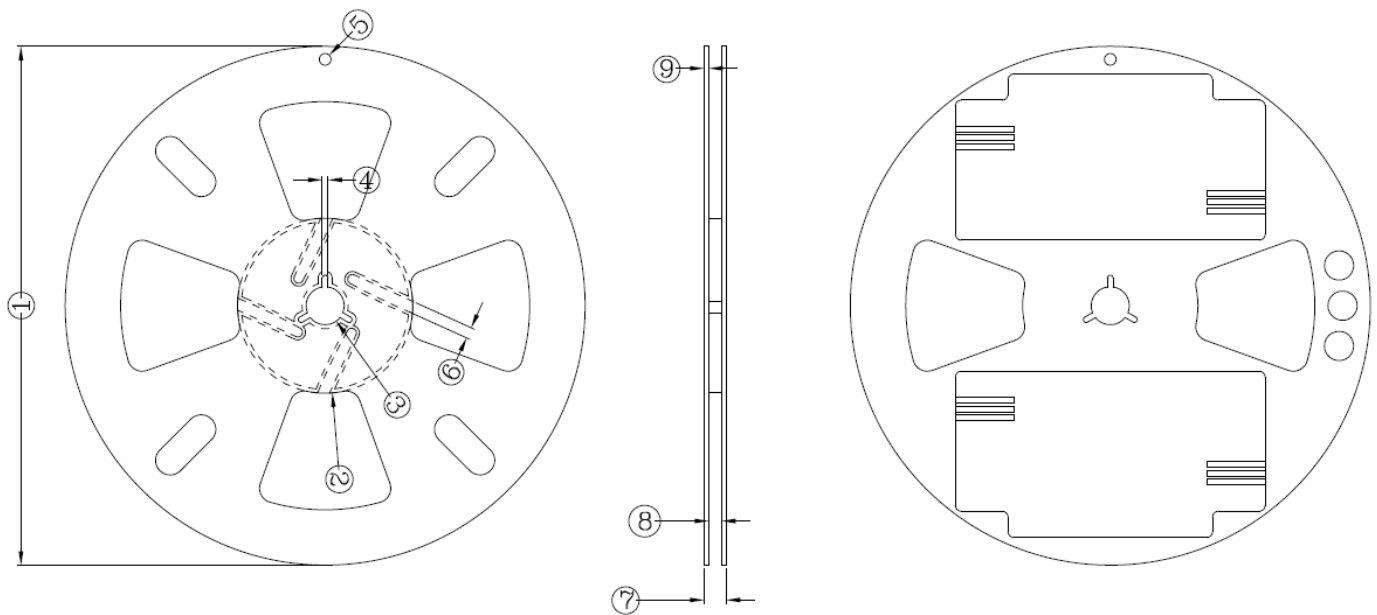


Fig. 6.1-3

Table 6.1-2

Unit:mm

①	②	③	④	⑤	⑥	⑦	⑧	⑨
178±1.0	60±1.0	13±0.3	2.0±0.5	4±0.2	4±0.5	7.5±1	4.5±0.5	1.5±0.3

6.2 Storage

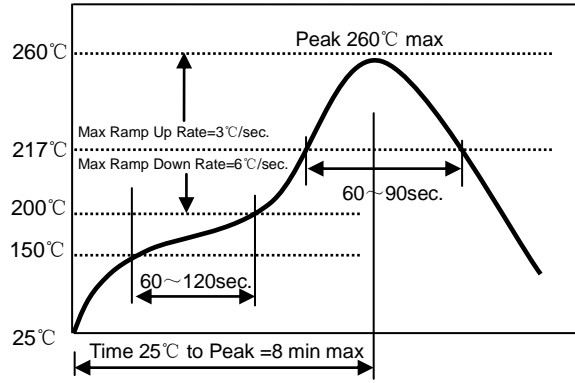
- a. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to Super humidity. Package must be stored at 40℃ or less and 70% RH or less.
- b. The solderability of the external electrode may be deteriorated if packages are stored where they are exposed to dust of harmful gas (e.g. HCl, sulfurous gas of H₂S).
- c. Packaging material may be deformed if package are stored where they are exposed to heat of direct sunlight.
- d. Solderability specified in **Clause 5.4.6** shall be guaranteed for 12 months from the date of delivery on condition that they are stored at the environment specified in **Clause 3**. For those parts, which passed more than 12 months shall be checked solder-ability before use.

7. Recommended Soldering Technologies

7.1 Reflow Profile

- △ Preheat condition: 150 ~200°C/60~120sec.
- △ Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- △ Max time at max temp: 10sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Allowed Reflow time: 2x max

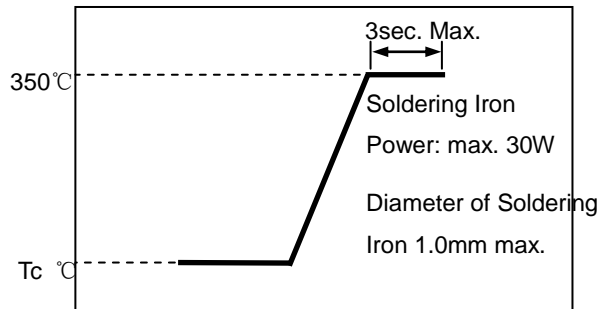
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]



7.2 Iron Soldering Profile

- △ Iron soldering power: Max. 30W
- △ Pre-heating: 150°C/60sec.
- △ Soldering Tip temperature: 350°C Max.
- △ Soldering time: 3sec. Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Max.1 times for iron soldering

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]



Appendix A: Electrical Characteristics (SDCL0402Q-P01 Series of Inductors)

SDCL0402Q-P01 Series of Inductor

Part Number	Inductance	Min. Quality Factor	L, Q Test Freq. L/Q	Typical Q @ Freq. (GHz)					Min. Self-resonant Frequency	Max. DC Resistance	Max. Rated Current	Thickness
				0.5	0.8	1.8	2.0	2.4				
Units	nH	-	MHz	-					MHz	Ω	mA	mm [inch]
Symbol	L	Q	Freq	Q					S.R.F	DCR	I _r	T
SDCL0402Q0N3P01	0.3	14	500	/	/	/	/	/	17000	0.03	990	0.23±0.02 [.009±.0008]
SDCL0402Q0N4P01	0.4	14	500	/	/	/	/	/	17000	0.04	990	
SDCL0402Q0N5P01	0.5	14	500	/	/	/	/	/	17000	0.04	990	
SDCL0402Q0N6P01	0.6	14	500	20	25	36	38	45	16600	0.05	900	
SDCL0402Q0N7P01	0.7	14	500	20	25	36	38	45	16600	0.05	900	
SDCL0402Q0N8P01	0.8	14	500	20	25	37	39	46	16600	0.07	900	
SDCL0402Q0N9P01	0.9	14	500	19	25	36	38	45	15500	0.1	600	
SDCL0402Q1N0P01	1.0	14	500	19	25	36	38	45	15500	0.1	600	
SDCL0402Q1N1P01	1.1	14	500	19	25	36	38	45	15500	0.11	550	
SDCL0402Q1N2P01	1.2	14	500	19	24	35	37	44	15500	0.11	550	
SDCL0402Q1N3P01	1.3	14	500	19	24	35	37	44	15500	0.11	550	
SDCL0402Q1N4P01	1.4	14	500	19	25	36	38	45	15000	0.12	450	
SDCL0402Q1N5P01	1.5	14	500	19	24	35	37	43	15000	0.12	450	
SDCL0402Q1N6P01	1.6	14	500	19	23	35	36	43	15000	0.15	450	
SDCL0402Q1N7P01	1.7	14	500	19	24	35	36	43	15000	0.15	450	
SDCL0402Q1N8P01	1.8	14	500	19	24	35	37	44	13000	0.15	450	
SDCL0402Q1N9P01	1.9	14	500	19	24	35	37	44	12000	0.16	450	
SDCL0402Q2N0P01	2.0	14	500	19	25	35	38	45	11000	0.16	450	
SDCL0402Q2N1P01	2.1	14	500	19	25	35	37	44	11000	0.16	450	
SDCL0402Q2N2P01	2.2	14	500	19	25	35	37	43	10500	0.18	400	
SDCL0402Q2N3P01	2.3	14	500	19	24	34	36	43	10500	0.18	400	
SDCL0402Q2N4P01	2.4	14	500	19	25	37	39	46	10500	0.2	400	
SDCL0402Q2N5P01	2.5	14	500	19	24	35	36	43	10000	0.2	400	
SDCL0402Q2N6P01	2.6	14	500	19	24	35	36	43	10000	0.2	400	
SDCL0402Q2N7P01	2.7	14	500	19	24	37	39	43	9500	0.23	350	
SDCL0402Q2N8P01	2.8	14	500	19	24	37	40	46	9500	0.23	350	
SDCL0402Q2N9P01	2.9	14	500	19	24	36	39	45	9500	0.23	350	
SDCL0402Q3N0P01	3.0	14	500	19	25	36	38	45	9500	0.26	350	
SDCL0402Q3N1P01	3.1	14	500	19	25	35	37	43	9000	0.26	350	
SDCL0402Q3N2P01	3.2	14	500	19	24	35	37	44	9000	0.26	350	
SDCL0402Q3N3P01	3.3	14	500	19	25	36	38	45	9000	0.26	350	
SDCL0402Q3N4P01	3.4	14	500	19	24	35	38	44	9000	0.26	350	
SDCL0402Q3N5P01	3.5	14	500	19	25	36	38	45	8700	0.28	350	
SDCL0402Q3N6P01	3.6	14	500	19	24	35	37	44	8700	0.28	350	
SDCL0402Q3N7P01	3.7	14	500	19	24	35	37	44	8700	0.28	350	
SDCL0402Q3N8P01	3.8	14	500	19	24	34	36	42	8700	0.28	350	
SDCL0402Q3N9P01	3.9	14	500	18	23	33	35	39	8700	0.3	350	
SDCL0402Q4N0P01	4.0	14	500	18	23	33	35	40	8000	0.3	350	
SDCL0402Q4N1P01	4.1	14	500	18	23	33	35	40	7500	0.3	350	
SDCL0402Q4N2P01	4.2	14	500	18	23	34	36	41	7000	0.3	350	

Part Number	Inductance	Min. Quality Factor	L, Q Test Freq .L/Q	Typical Q @ Freq. (GHz)					Min .Self-resonant Frequency	Max. DC Resistance	Max .Rated Current	Thickness
				0.5	0.8	1.8	2.0	2.4				
Units	nH	-	MHz	-					MHz	Ω	mA	mm [inch]
Symbol	L	Q	Freq	Q					S.R.F	DCR	I _r	T
SDCL0402Q4N3□P01	4.3	14	500	18	22	33	35	40	7000	0.3	350	0.23±0.02 [.009±.0008]
SDCL0402Q4N7□P01	4.7	14	500	18	23	34	36	42	7000	0.4	300	
SDCL0402Q5N1□P01	5.1	14	500	17	22	32	34	37	6600	0.4	300	
SDCL0402Q5N6□P01	5.6	14	500	17	22	31	33	36	6100	0.4	300	
SDCL0402Q6N2□P01	6.2	14	500	17	22	32	33	38	6000	0.45	300	
SDCL0402Q6N8□P01	6.8	14	500	17	21	30	32	35	5700	0.52	250	
SDCL0402Q7N5□P01	7.5	14	500	16	20	29	31	34	5500	0.68	230	
SDCL0402Q8N2□P01	8.2	14	500	17	21	30	32	35	5300	0.68	230	
SDCL0402Q9N1□P01	9.1	14	500	16	20	29	32	35	5000	0.8	170	
SDCL0402Q10N□P01	10	14	500	16	20	29	31	34	4500	0.85	170	
SDCL0402Q11N□P01	11	14	500	16	21	28	30	31	4200	0.9	170	
SDCL0402Q12N□P01	12	14	500	16	20	27	28	29	4000	0.93	170	
SDCL0402Q13N□P01	13	12	500	15	18	25	26	27	3800	1.2	160	
SDCL0402Q15N□P01	15	12	500	15	18	24	25	25	3500	1.8	140	
SDCL0402Q16N□P01	16	12	500	15	18	24	25	25	3500	1.8	140	
SDCL0402Q18N□P01	18	9	500	11	15	18	20	19	3000	2.5	140	
SDCL0402Q20N□P01	20	9	500	11	15	18	20	19	2700	2.8	140	
SDCL0402Q22N□P01	22	9	500	11	14	17	20	18	2300	3.5	120	

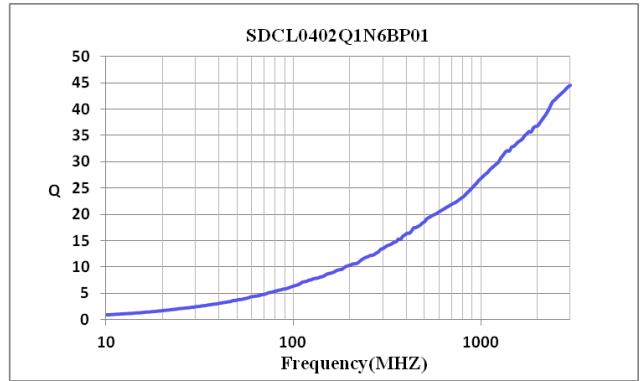
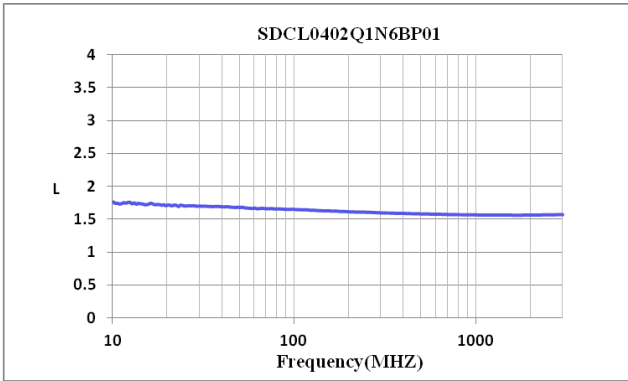
Note: □: Please specify the inductance tolerance. For $L \leq 4.2\text{nH}$, choose $B = \pm 0.1\text{nH}$, $C = \pm 0.2\text{nH}$ or $S = \pm 0.3\text{nH}$; For $4.2\text{nH} < L < 5.6\text{nH}$, choose, $H = \pm 3\%$, $J = \pm 5\%$. or $S = \pm 0.3\text{nH}$; For $L \geq 5.6\text{nH}$, choose, $H = \pm 3\%$, $J = \pm 5\%$

TYPICAL ELECTRICAL CHARACTERISTICS

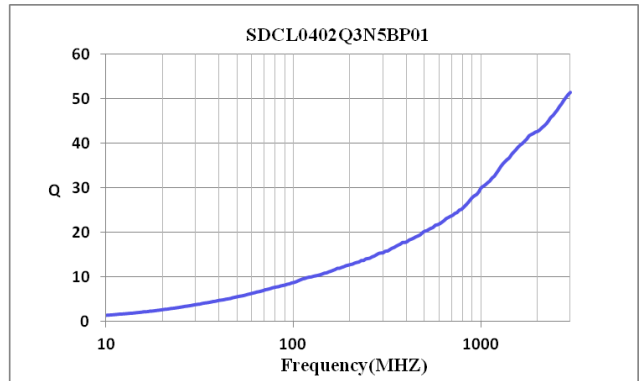
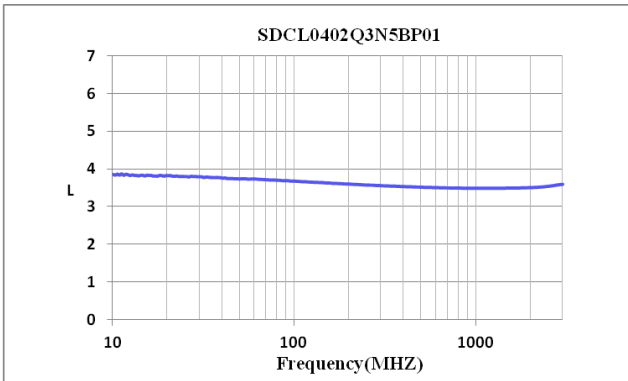
Inductance-Frequency Characteristics(Typ.)

Q-Frequency Characteristics(Typ.)

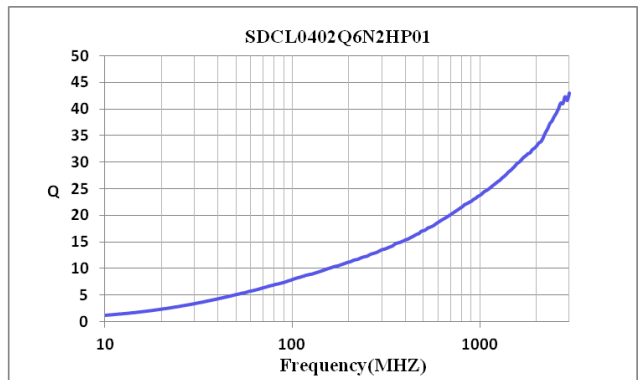
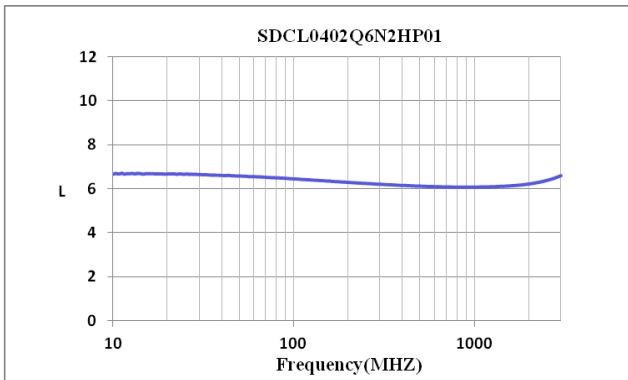
SDCL0402Q1N6BP01



SDCL0402Q3N5BP01



SDCL0402Q6N2HP01



SDCL0402Q12NHP01

